

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10631921			
<b>Filing Date:</b>	31-Jul-2003			
<b>Title of Invention:</b>	TRANSISTOR GATE AND LOCAL INTERCONNECT			
First Named Inventor/Applicant Name:	Jigish D. Trivedi			
<b>Filer:</b>	James W. Ausley/Heather Clarke			
<b>Attorney Docket Number:</b>	MICRON.104DV1			
Filed as Large Entity				
<b>Utility Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
Post-Allowance-and-Post-Issuance:				
Certificate of correction	1811	1	100	100
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				100